				<del>-</del> -	
AMENDMENT TRANSMITTAL LETTER (Large Ent Applicant(s): Shuichi Takayama et al.			~	Docket No. NAK1-BG86	
Serial No. 09/301,284	1	g Date 28, 1999 25	waminer T. Vo		Group Art Unit
Invention: PROCESSOR FOR EXECUTING INSTRUCTION UNITS THAT ARE UNRELATED TO THE UNITS					
IN WHICH INSTRUCTIONS ARE READ AND A COMPILER, AN OPTIMIZATION APPARATUS, AN ASSEMBLER,					
A LINKER, A DEBUGGER, AND A DISASSEMBLER FOR SUCH A DEBUGGER					
TO THE ASSISTANT COMMISSIONER FOR PATENTS:					
Transmitted herewith is an amendment in the above-identified application. $0CT 0 1 2002$					
The fee has been calculated and is transmitted as shown below.				T	echnology Center 2100
CLAIMS AS AMENDED					
	CLAIMS REMAINING	HIGHEST #	NUMBER EXTRA	RATE	ADÓITIONAL
	AFTER AMENDMENT	PREV. PAID FOR	CLAIMS PRESENT	040.0	FEE
TOTAL CLAIMS	12 -	48 =	. 0	x \$18.0	
INDEP. CLAIMS	2 -	11 =	0	x \$84.0	
Multiple Dependent Claims (check if applicable)					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT \$0.00					
No additional fee is required for amendment.  □ Please charge Deposit Account No. in the amount of A duplicate copy of this sheet is enclosed.  ☑ A check in the amount of \$400.00 to cover the Request for Two Month Extension of Time Fee ☑ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 16-2462 A duplicate copy of this sheet is enclosed. ☑ Any additional filling fees required under 37 C.F.R. 1.16. ☑ Any patent application processing fees under 37 CFR 1.17.  □ Dated: September 20, 2002					
Reg. No. 25, 1/24/ PRICE and GESS 2100 S.E. Main St Irvine, CA 92614 Tel: 949-261-843	treet, Suite 250		on 9/20/02 first class ma Assistant Co 20231 Sign	ail under 37 C. commissioner 1  Autority of Person  Mare	ent and fee is being deposited with the U.S. Postal Service as F.R. 1.8 and is addressed to the for Patents, Washington, D.C. Mailing Correspondence
Typed or Printed Name of Person Mailing Corresponde					



RESPONSE UNDER 37 CFR 1.116 EXPEDITED PROCEDURE - GROUP 2122

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Serial No.: 09/301,284

Shuichi Takayama et al.

Filed: April 28, 1999

For: PROCESSOR FOR EXECUTING

INSTRUCTIONS IN UNITS THAT ARE UNRELATED TO THE UNITS IN WHICH

INSTRUCTIONS ARE READ AND A COMPILER, AN OPTIMIZATION

APPARATUS, AN ASSEMBLER, A LINKER,

A DEBUGGER, AND A DISASSEMBLER

FOR SUCH A DEBUGGER

Examiner: T. Vo

Group Art Unit: 2122

Irvine, California 92614

September 20, 2002

RECEIVED

OCT 0 1 2002

Technology Center 2100

## **AMENDMENT**

Honorable Assistant Commissioner for Patents

Box AF

Washington, D.C. 20231

Dear Sir:

In response to the Office Action mailed April 23, 2002, please amend the above-identified application as follows:

## **IN THE CLAIMS:**

Please amend the claims as follows:

1. (Twice Amended) A processor for reading instructions from a memory according to a program counter, and for executing the read instruction,